

	L #	Hit	S arch T xt	DBs
1	L1	2065	(361/767,768,777,779,803).ccls.	USP AT; US-P GPU B
2	L2	1324	(174/262,263).ccls.	USP AT; US-P GPU B
3	L3	3148	1 2	USP AT; US-P GPU B
4	L4	156	3 and (castellation\$1 castellating notch\$2)	USP AT; US-P GPU B
5	L5	497	3 and (groove\$1 channel\$1)	USP AT; US-P GPU B
6	L6	447	5 not 4	USP AT; US-P GPU B